FIG. 1

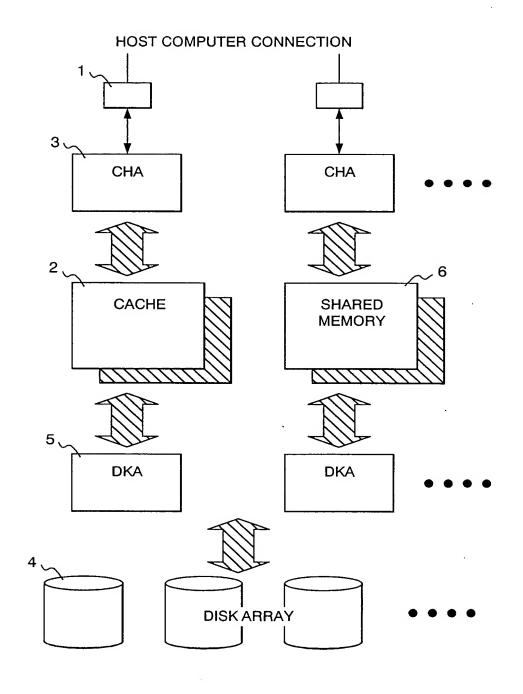


FIG. 2

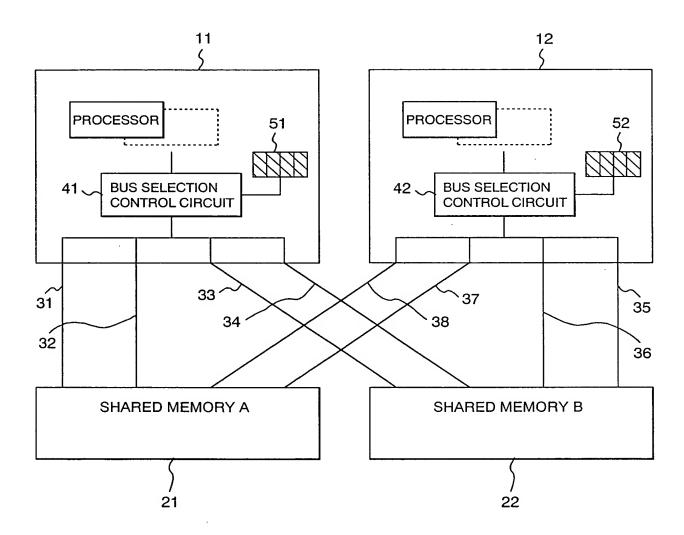


FIG. 3

SET VALUE OF EACH BIT OF THE REGISTER				ACCESS PATH
A-1	A-2	B-1	B-2	
0	0	0	0	PLANE A = DISTRIBUTED PATH, PLANE B = DISTRIBUTED PATH
1	0	0	0	PLANE A = USES PATH A-1, PLANE B = DISTRIBUTED PATH
0	1	0	0	PLANE A = USES PATH A-2, PLANE B = DISTRIBUTED PATH
0	. 0	1	0	PLANE A = DISTRIBUTED PATH, PLANE B = USES PATH B-1
0	0	0	1	PLANE A = DISTRIBUTED PATH, PLANE B = USES PATH B-2
1	0	1	0	PLANE A = USES PATH A-1, PLANE B = USES PATH B-1
1	0	0	1	PLANE A = USES PATH A-1, PLANE B = USES PATH B-2
0	1	1	0	PLANE A = USES PATH A-2, PLANE B = USES PATH B-1
0	1	0	1	PLANE A = USES PATH A-2, PLANE B = USES PATH B-2

FIG. 4

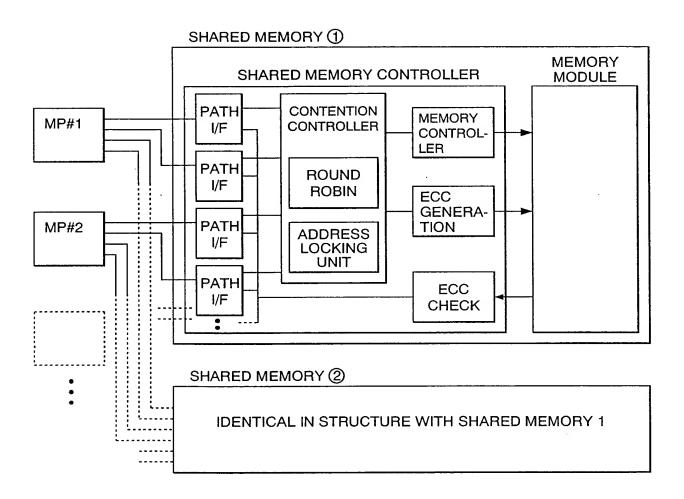


FIG. 5

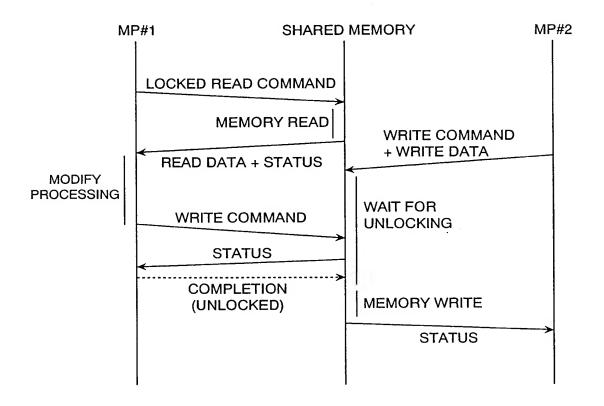


FIG. 6

AC	CESS TES	T RESULTS	6	ASSUMED FAULT LOCATION
PATH 1A (a)	PATH 1A (b)	PATH 1B (a)	PATH 1B (b)	
ок	ок	ОК	ОК	NO FAULT
NG	ОК	OK	ОК	PATH 1A (a)
ОК	NG	ОК	ОК	PATH 1A (b)
ок	ОК	NG	ОК	PATH 1B (a)
ОК	ОК	ОК	NG	PATH 1B (b)
NG	NG	ок	ок	SHARED MEMORY A
NG	ОК	NG	ОК	FAULT MULTIPLEXED ? (PROCESSOR)
NG	ок	ОК	NG	FAULT MULTIPLEXED ? (PROCESSOR)
ОК	NG	NG	ок	FAULT MULTIPLEXED ? (PROCESSOR)
ОК	NG	ок	NG	FAULT MULTIPLEXED ? (PROCESSOR)
ОК	ОК	NG	NG	SHARED MEMORY B
NG	NG	NG	ок	FAULT MULTIPLEXED ? (PROCESSOR)
NG	NG	ок	NG	FAULT MULTIPLEXED ? (PROCESSOR)
NG	ок	NG	NG	FAULT MULTIPLEXED ? (PROCESSOR)
ОК	NG	NG	NG	FAULT MULTIPLEXED ? (PROCESSOR)
NG	NG	NG	NG	PROCESSOR

FIG. 7

